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PATENT  
158-956

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:  
Matthew J. Amatangelo, et al.

Serial No.: 09/888,474

Group Art Unit: 2123

Filed: June 25, 2001

Examiner: Unknown

FOR: Method of Utilizing Timing  
Models to Provide Data for  
Static Timing Analysis of  
Electronic Circuits

Commissioner for Patents  
Washington, D.C. 20231

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SEP 19 2001  
Technology Center 2100

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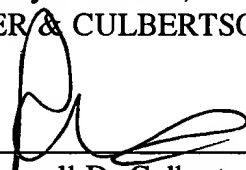
The following items are enclosed for filing in the above-identified patent application:

- ☒ Form PTO/SB/08A, along with copies of listed references  
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If there is any fee due please charge such fee to Deposit Account No. 19-1453 (Our File No. 158-956).

Respectfully submitted,  
SHAFFER & CULBERTSON, L.L.P.

Dated: 11 Sept 2001

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**CERTIFICATE OF MAILING**

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(use as many sheets as necessary)*

Sheet

1

of

2

Application Number

09/888.474

Filing Date

June 25, 2001.

**First Named Inventor**

**Matthew J. Amatangelo**

### Group Art Unit

Examiner Name

Attorney Docket Number

AUS9-2001-0049-US1

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

Examiner  
Signature

Date \_\_\_\_\_

**Considered**

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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## Application Number

Filing Date

**First Named Inventor**

Group Art Unit

Examiner Name

Attorney Docket Number

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